|  | Application No.  | Applicant(s)  |                           |  |
|--|--|---|---------------------------|--|
|  | Application No.  | ,   |                           |  |
| Notice of Allowability   | 10/650,653   | RAJASHEKARA ET  | AL.                       |  |
|  | Examiner   | Art Unit  |                           |  |
|  | Michael Rutland-Wallis   | 2835  | <u> </u>                  |  |
| The MAILING DATE of this communication app<br>All claims being allowable, PROSECUTION ON THE MERITS I<br>herewith (or previously mailed), a Notice of Allowance (PTOL-8:<br>NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT<br>of the Office or upon petition by the applicant. See 37 CFR 1.3:  | S (OR REMAINS) CLOSED in this is 5) or other appropriate communicat RIGHTS. This application is subjection is subjection in the control of th | application. If not includ<br>ion will be mailed in due                               | ed<br>course. <b>THIS</b> |  |
| 1. This communication is responsive to <u>08/28/2003</u> .   |  |   |                           |  |
| 2. $\boxtimes$ The allowed claim(s) is/are <u>1-21</u> .   |  |   |                           |  |
| <ol> <li>Acknowledgment is made of a claim for foreign priority         <ul> <li>All b)</li></ul></li></ol>  | ve been received.<br>ve been received in Application No.   |   | ation from the            |  |
| Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.  | E" of this communication to file a repNMENT of this application.   | oly complying with the re   | equirements               |  |
| 4. A SUBSTITUTE OATH OR DECLARATION must be sub INFORMAL PATENT APPLICATION (PTO-152) which g  | omitted. Note the attached EXAMIN ives reason(s) why the oath or decl  | ER'S AMENDMENT or I aration is deficient.   | NOTICE OF                 |  |
| <ul> <li>5. CORRECTED DRAWINGS ( as "replacement sheets") m</li> <li>(a) including changes required by the Notice of Draftsper 1) hereto or 2) to Paper No./Mail Date</li> <li>(b) including changes required by the attached Examined Paper No./Mail Date</li> <li>Identifying indicia such as the application number (see 37 CFF each sheet. Replacement sheet(s) should be labeled as such in the department of the depar</li></ul> | erson's Patent Drawing Review (PT er's Amendment / Comment or in the R 1.84(c)) should be written on the dra n the header according to 37 CFR 1.1  | e Office action of awings in the front (not the 21(d).  L must be submitted.          |                           |  |
| <ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SI Paper No./Mail Date 8/28/03 &amp; 1/15/04</li> <li>4. ☐ Examiner's Comment Regarding Requirement for Depos of Biological Material</li> </ul>   | 8) 6. Interview Summ<br>Paper No./Mail<br>B/08), 7. Examiner's Ame   | al Patent Application (PT ary (PTO-413), Date Indment/Comment ement of Reasons for Al |                           |  |
|  | SUPERVISORY PATENT FYAMINER  TECHNOLOGY CENTER 2800  |   |                           |  |

## **DETAILED ACTION**

## Allowable Subject Matter

Claims 1-21 are allowed. The following is an examiner's statement of reasons for allowance: Thomas et al. (U.S. Pat. No. 6,160,722) teaches a system for transferring power comprising a first power source (item 10) having a first pair of terminals associated therewith; a second power source (item 460) having a second pair of terminals associated therewith (i) a first pair of switches (items Q1 and Q2) connected to the first pair of terminals, while Thomas utilizes BJT transistors It would have been obvious to one of ordinary skill in the art at the time of the invention to use MOSFET having a source, drain and gate terminals connected as claimed. Thomas teaches terminal of the lower switch (Q2) thereby forming a first common node, and wherein each of the first pair of switches has an anti-parallel diode associated therewith; (ii) a second pair of switches (items Q2 and Q3) connected to the second pair of terminals, wherein said source terminal of said upper switch is electrically connected to the upper terminal of the lower switch thereby forming a second common node, and wherein each of the second pair of switches has an anti-parallel diode associated therewith; (iii) a third pair of switches (items Q3 and Q4), said third switch pair comprising an upper switch wherein terminal of said upper switch is electrically connected to the upper terminal of the lower switch thereby forming a third common node, and wherein each of the third pair of switches has an anti-parallel diode associated therewith; (iv) a first inductor (item

L1) a second inductor (item L2); and (vi) a control unit (item 440) configured to actuate the switches in the first, second, and third pairs of switches in accordance with one of a plurality of modes of operation for transferring power between at least a pair of said first, second. Thomas does not utilize three power sources nor does Thomas teach the limitations of the first and second inductors in the claimed connection. Nino (U.S. Pub. No. 20050218876) teaches a first inductor coupled between a first set of switches and a second set of switched but does not teach the second inductor or the additional power sources lacking in Thomas. At least for these further limitations to independent claims 1,8 and 15 are not taught or rendered obvious by the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael Rutland-Wallis whose telephone number is 571-272-5921. The examiner can normally be reached on Monday-Thursday 7:30AM-6:00PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynn D. Feild can be reached on 571-272-2092. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Application/Control Number: 10/650,653

Art Unit: 2835

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MRW

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